

FIG. 1 is a block diagram of a system architecture for a command and control system. The system includes a source of data (12) and a receiver of data (24). The source of data (12) sends data in J2EE compliant format (14) to the system. The system includes a command and control system (10) which contains a track management system (18). The track management system (18) includes an EJB software component arrangement (20) and an application server arrangement (16). The EJB software component arrangement (20) includes EJBs (20a, 20b, ..., 20n). The application server arrangement (16) includes application servers (16a, 16b, ..., 16M). The application server arrangement (16) also includes a computer processing arrangement (22) which includes CPUs (22a, 22b, ..., 22N). The system also includes a receiver of data (24) which receives data in J2EE compliant format (26) from the system.

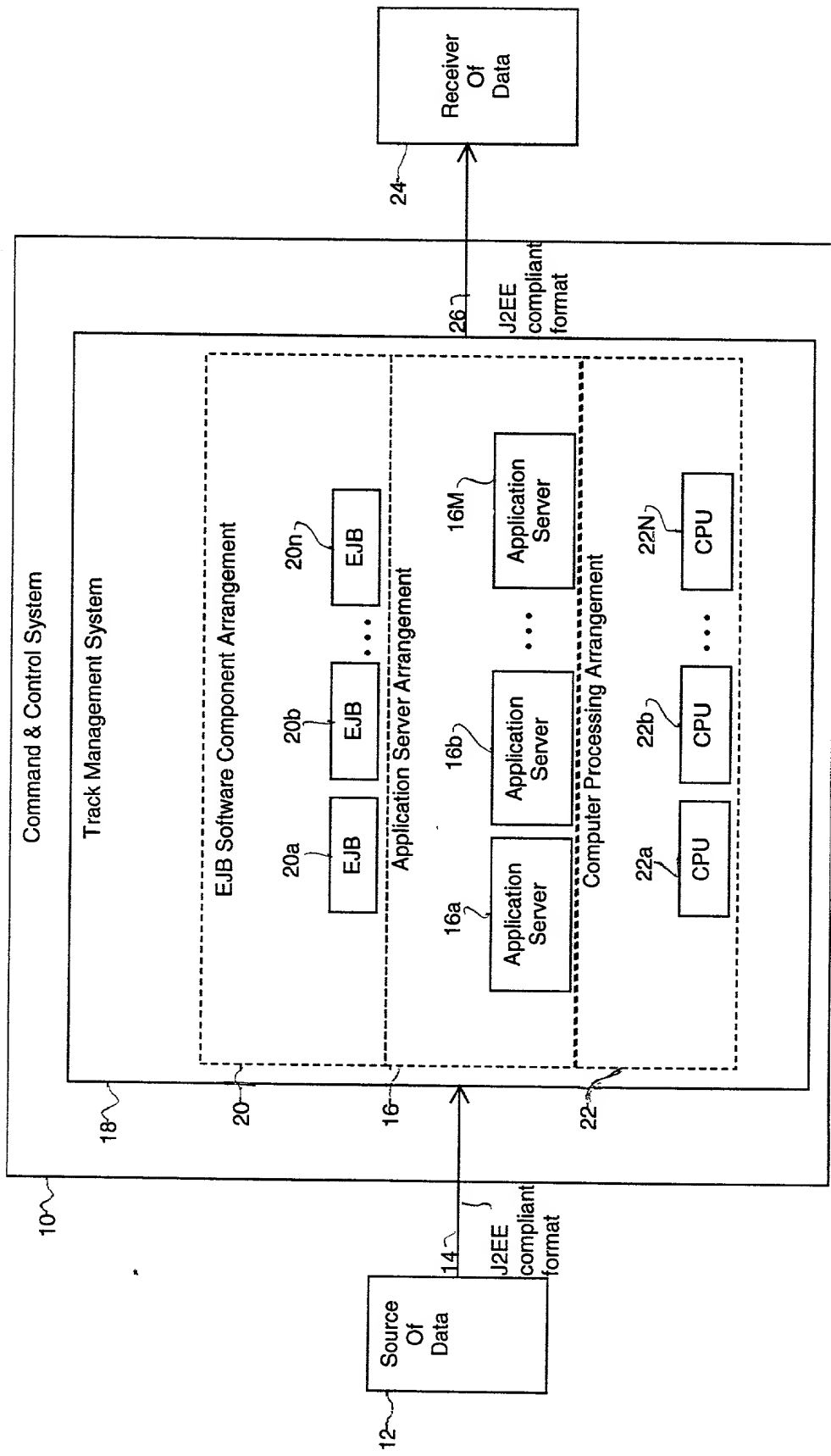


Figure 1.

Track Management System

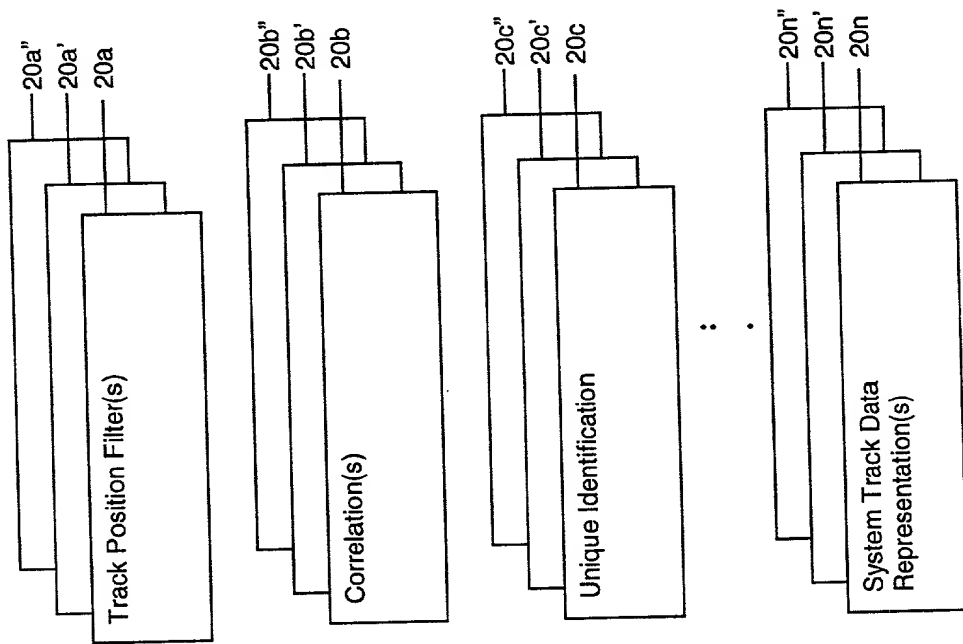


Figure 2.

1:1

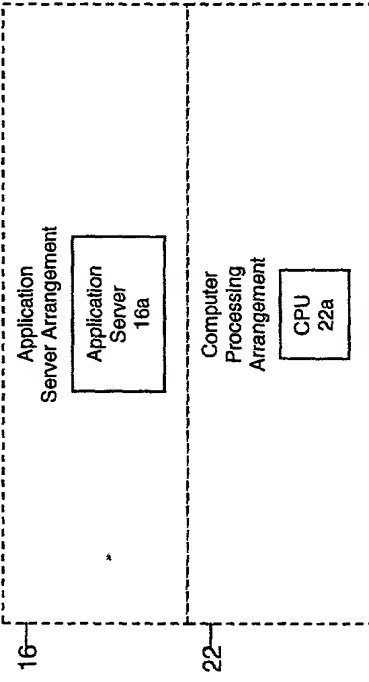


Figure 3a. One Application Server to One CPU

1:N

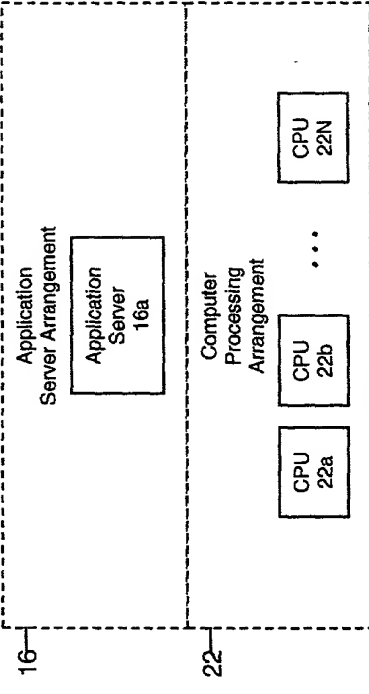


Figure 3b. One Application Server to Many CPUs

M:1

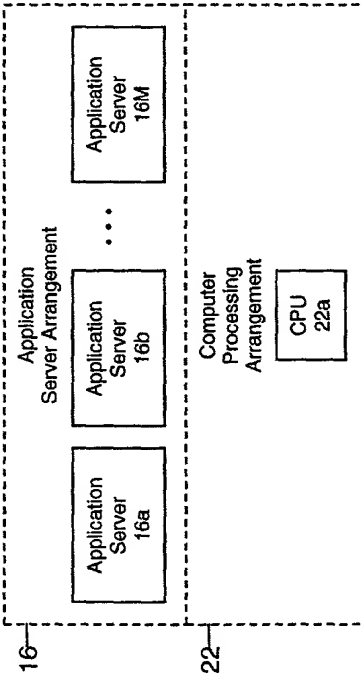


Figure 3c. Many Application Servers to One CPU

M:N

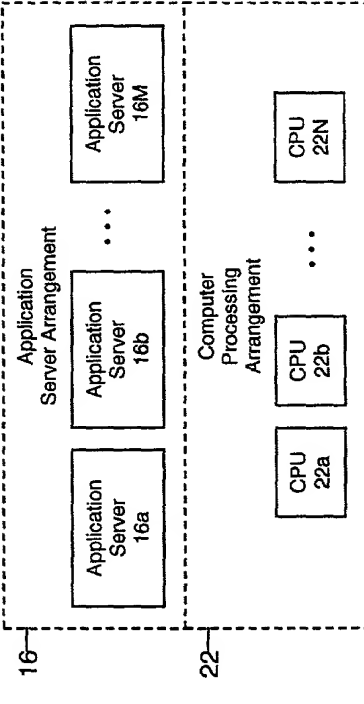


Figure 3d. Many Application Servers to Many CPUs

FIG. 4a is a block diagram of a system architecture. The system is divided into two main sections: a top section labeled 'EJB Software Component Arrangement' and a bottom section labeled 'Application Server Arrangement'. The top section contains a single box labeled 'EJB 20a'. The bottom section contains a single box labeled 'Application Server 16a'. The entire system is enclosed in a dashed box with a reference numeral '20' at the top left and '16' at the bottom left.

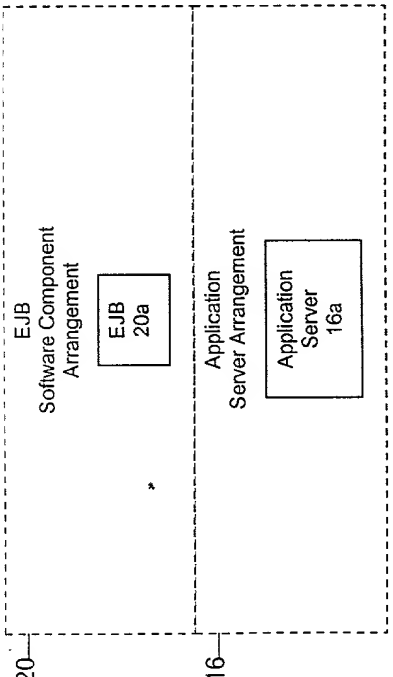


Figure 4a

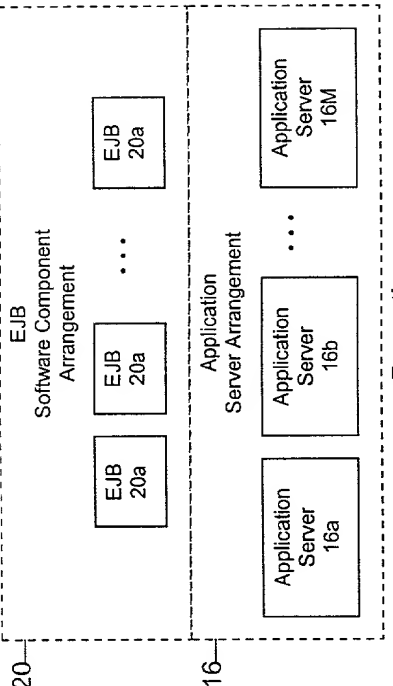


Figure 4b

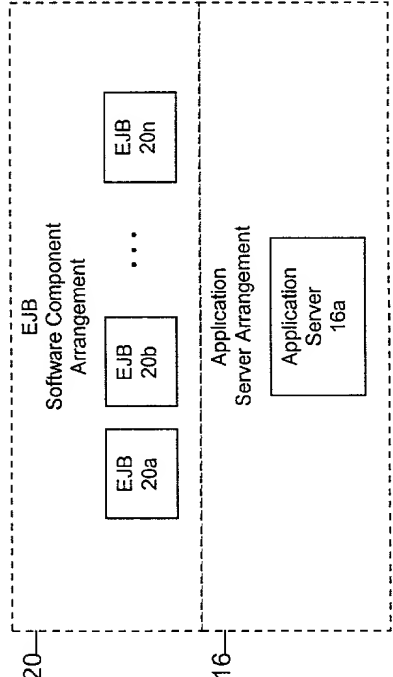


Figure 4c

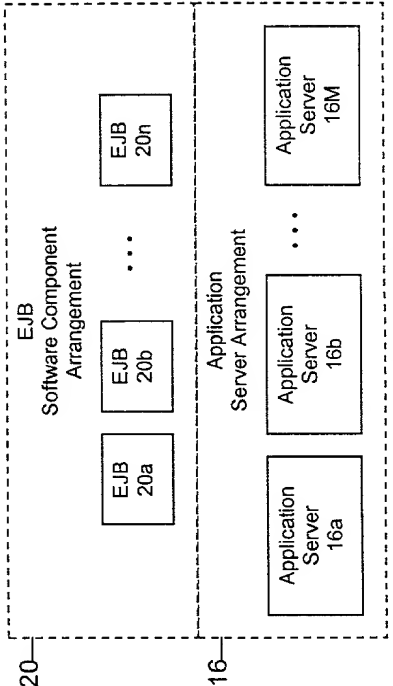


Figure 4d

1. The present invention relates to a computer system and method for processing data.

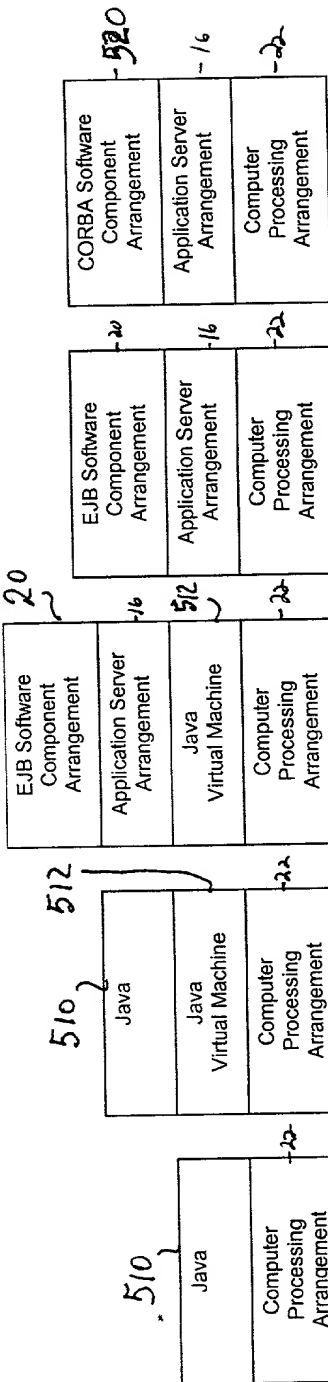


Figure 5a

Figure 5b

Figure 5c

Figure 5d

Figure 5e